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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

363  
32895

APPL NUM 10051562	FILING DATE 01/18/2002	CLASS 43825	SUBCLASS 347	GAU 2822	EXAMINER Novack
<b>**APPLICANTS:</b> Chan Kevin; Cohen Guy; Taur Yuan; Wong Hon-Sum;					
<b>**CONTINUING DATA VERIFIED:</b> THIS APPLICATION IS A DIV OF 09/272,297 03/19/1999 <i>yes cx</i>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> <i>none cx</i>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO YO998-426DIV	
Verified and Acknowledged Examiners's initials <i>CX</i>					
TITLE : Self-aligned double-gate MOSFET by selective epitaxy and silicon wafer bonding techniques					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Assistant Examiner	Total Claims
			Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		Primary Examiner	Applicati n Examiner
		<b>PREPARED FOR ISSUE</b>	
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